

**INSERTION/DELETION/SUBSTITUTION ERROR
CORRECTION CAPABILITIES OF LDPC CODES USING
SLIDE DECODING**

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This study investigates insertion, deletion, and substitution error correcting codes constructed with lowdensity parity-check (LDPC) codes. The studies for insertion/deletion/substitution error correcting codes are few comparing with substitution error correcting codes. We improve insertion/deletion/substitution error correction capabilities of LDPC codes using slide decoding for modified check matrix. Our evaluation shows that the block error rate of the proposed method is lower than that of the conventional method using marker codes.